

TRANSCEIVER MODULE AND INTEGRATED CIRCUIT WITH DUAL EYE OPENERS AND INTEGRATED LOOPBACK AND BIT ERROR RATE TESTING

ABSTRACT OF THE DISCLOSURE

A transceiver module having integrated eye diagram opening functionality for reducing jitter is described. The transceiver module may include a transmitter eye opener and a receiver eye opener integrated in a single circuit. The transceiver module may also include serial control and various other integrated components. Other functionalities that may be integrated on the transceiver module include loopback modes, bypass features, bit error rate testing, and power down modes.